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MPG:ejv 10/20/05 7126-72254-01 3419/05

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

|                        |                                         |
|------------------------|-----------------------------------------|
| Attorney Docket Number | 7126-72254-01                           |
| Application Number     | <del>Not yet assigned</del> 11/554,139  |
| Filing Date            | Herewith 02/01/2008                     |
| First Named Inventor   | Hammouda                                |
| Art Unit               | <del>Not yet assigned</del> 2825        |
| Examiner Name          | <del>Not yet assigned</del> Lin, Sun J. |

## **U.S. PATENT DOCUMENTS**

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

| Examiner's<br>Initials* | Cite No.<br>(optional) | Number | Publication Date | Name of Applicant or Patentee |
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## **FOREIGN PATENT DOCUMENTS**

| Examiner's<br>Initials* | Cite No.<br>(optional) | Country | Number | Publication Date | Name of<br>Applicant or Patentee |
|-------------------------|------------------------|---------|--------|------------------|----------------------------------|
|                         |                        |         |        |                  |                                  |
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## **OTHER DOCUMENTS**

| Examiner's<br>Initials* | Cite No.<br>(optional) |                                                                                                                                   |
|-------------------------|------------------------|-----------------------------------------------------------------------------------------------------------------------------------|
| /JSL/                   |                        | Dessouky, M. et al., "Analog IP Design Retargeting based on Design Extraction and Transistor Optimization," October 2004, 17 pgs. |
| /JSL/                   |                        | Hammouda, S. et al., "A Fully Automated Approach for Analog Circuit Reuse," July 2004, 4 pgs.                                     |
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EXAMINER  
SIGNATURE: /Sun J. Lin/

DATE  
CONSIDERED: 05/11/2009

\* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.